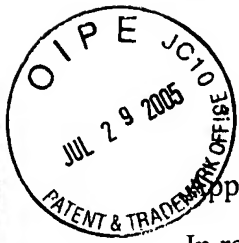


08-01-05

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Attorney Docket: TSMC2002-0115 (N1085-90179) *fw*



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/785,522

Confirmation No.: 8308

In re application of: Yeu-Der Chih

Filed: February 24, 2004

Technology Center/Art Unit: 2827

Examiner: Huan Hoang

Docket No.: TSMC2002-0115 (N1085-90179)

Customer No.: 08933

Title: AN ARRAY STRUCTURE OF TWO-
TRANSISTOR CELLS WITH MERGED
FLOATING GATES FOR BYTE ERASE
AND RE-WRITE IF DISTURBED
ALGORITHM

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Steven E. Koffe

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Sir:

Responsive to the Office Action dated June 30, 2005, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.